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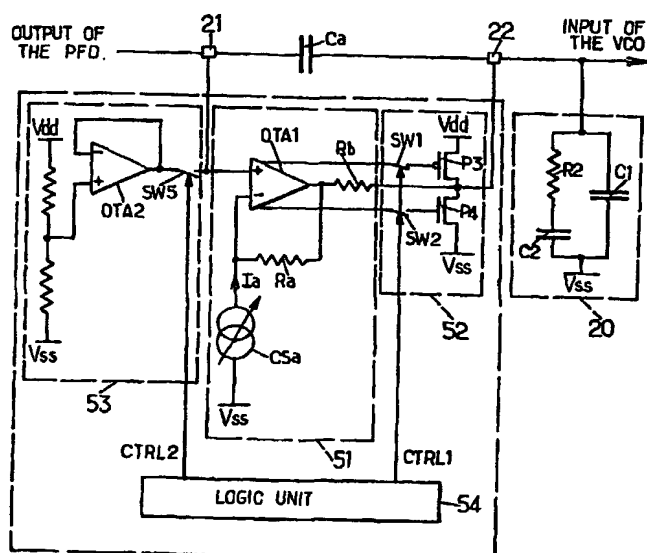
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(54) Title: VOLTAGE SHIFT CONTROL CIRCUIT FOR PLL



(57) Abstract: A PLL structure comprises a PFD, a loop filter and a VCO, as well as a voltage shift capacitor (Ca) coupling the PFD and the VCO. A voltage shift control circuit (50) is placed in parallel with the voltage shift capacitor. This circuit comprises controlled charging means (51), which are designed to charge the voltage shift capacitor according to a channel control signal. It also comprises controlled pre-charging means (52) which are designed to accelerate the charging of the voltage shift capacitor by the controlled charging means. It further comprises controlled biasing means (53), designed to ensure the bias of the input during the pre-charging of the voltage shift capacitor.

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INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 H03L7/187

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H03L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

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A	WO 01/76072 A (SCHECHINGER ALOIS ;WANNENMACHER VOLKER (DE); SIEMENS AG (DE)) 11 October 2001 (2001-10-11) page 3, line 11 - page 4, line 20; figure 7	1-14
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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

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Information on patent family members

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